

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	multilevel adj logic adj minimization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:32
L2	1195	logic adj3 vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:32
L3	206	logic adj3 minimization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:36
L4	8	2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:34
L5	147276	symmetry	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:34
L6	2	4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:36
L7	2979	vector same symmetry	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:38
L8	1520	(logic\$3 adj5 minimization) or (logic\$3 adj5 optimization)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:37

L9	2	7 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:37
L10	130	(vector same symmetry) same minimiz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:54
L11	3056	logic\$3 adj expression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 13:30
L12	0	10 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:39
L13	371	exploit\$3 adj6 symmetry	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:58
L14	2139	logic\$2 adj4 scheme	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:55
L15	1	13 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:55
L16	2	11 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:56

L17	358420	vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:58
L18	142	13 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 12:58
L19	57	(logic\$3 adj expression) same vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/03 13:30



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logic\* vector

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## Results Key:

**JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 A fuzzy logic vector control of induction motor***Mohamed, H.A.F.; Hew, W.P.;*

TENCON 2000. Proceedings, Volume: 3, 24-27 Sept. 2000

Pages:324 - 328 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(384 KB\)\]](#) **IEEE CNF****2 A vector memory system based on wafer-scale integrated memory arrays***Chiueh, T.-C.;*

Computer Design: VLSI in Computers and Processors, 1993. ICCD '93.

Proceedings., 1993 IEEE International Conference on, 3-6 Oct. 1993

Pages:284 - 288

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) **IEEE CNF****3 Efficient implementation techniques for vector memory systems***Tzi-cker Chiueh; Verma, M.; Padubidri, S.;*

Parallel Architectures, Algorithms and Networks, 1994. (ISPAN) International Symposium on, 14-16 Dec. 1994

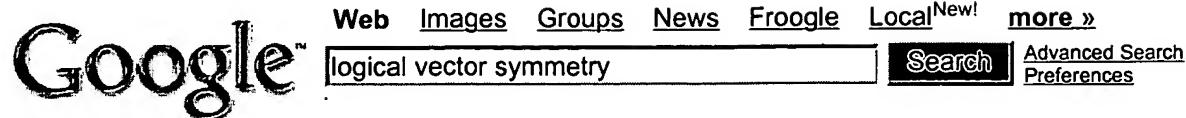
Pages:270 - 277

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) **IEEE CNF****4 Scalable binary sorting architecture based on rank ordering with area-time complexity***Hatirnaz, I.; Leblebici, Y.;*

ASIC/SOC Conference, 2000. Proceedings. 13th Annual IEEE International, Sept. 2000

Pages:369 - 373

[\[Abstract\]](#) [\[PDF Full-Text \(376 KB\)\]](#) **IEEE CNF****5 A new approach to fuzzy partitioning**

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VAC Manual on Using Axial Symmetry

... When these two conditions are met, the global **logical** variable polargrid is set to true. ... eg rotation of **vector** quantities around the **symmetry** axis. ...  
[www.phys.uu.nl/~toth/VACMan/axial.html](http://www.phys.uu.nl/~toth/VACMan/axial.html) - 10k - Cached - Similar pages

Answer to Pierre's Puzzle

... A bivector is the next step beyond **vector** in a **logical** progression: ... field by a **vector**, it would falsely suggest that **symmetry** had been lost. ...  
[www.av8n.com/physics/pierre-answer.htm](http://www.av8n.com/physics/pierre-answer.htm) - 14k - Cached - Similar pages

**[PDF] TS-LIB**

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 ... **Logical Vector Equal**. **Logical Vector Greater Than or Equal** ... **Check Matrix Symmetry**. Matrix Trace. Statistic Operations. **Vector Sum / Average** ...  
[www.transtech-dsp.com/datasheets/ts-lib\\_lm\\_v3.pdf](http://www.transtech-dsp.com/datasheets/ts-lib_lm_v3.pdf) - Similar pages

Crystal Systems and Bravais Lattices

... Now that we have considered **symmetry** in 2D we can apply the same concepts to 3D ... The various glide planes, their symbols and the displacement **vector** ...  
[www.chemistry.ohio-state.edu/~woodward/ch754/sym\\_3d.htm](http://www.chemistry.ohio-state.edu/~woodward/ch754/sym_3d.htm) - 41k - Cached - Similar pages

S-PLUS help

... a **logical** value indicating whether or not to test for **symmetry** (conjugate ... normality): a **logical vector** of length indicating whether or not to see if ...  
[www.uni-muenster.de/ZIV/Mitarbeiter/ BennoSueselbeck/s-html/helpfiles/Matrix.class.html](http://www.uni-muenster.de/ZIV/Mitarbeiter/ BennoSueselbeck/s-html/helpfiles/Matrix.class.html) - 4k - Cached - Similar pages

ORTEP-III Online Doc - Defs

... If the second atom in the atom designator run has the same **symmetry** and ... or a **vector** screened population as described for the sphere of enclosure. ...  
[www.ornl.gov/sci/ortep/doc/defs.html](http://www.ornl.gov/sci/ortep/doc/defs.html) - 13k - Cached - Similar pages

Another Symmetry

... because we're evaluating a difference in a **vector** field on two different bases, ... for any three **logical** variables X,Y,Z. Also, using the skew **symmetry** ...  
[www.meta-religion.com/Physics/ Relativity/another\\_symmetry.htm](http://www.meta-religion.com/Physics/ Relativity/another_symmetry.htm) - 40k - Cached - Similar pages

**[PDF] arXiv:hep-th/0004116 v1 16 Apr 2000**

File Format: PDF/Adobe Acrobat - View as HTML  
 ... completely determined by a new **vector symmetry** existing in its large topo-logical mass limit, the Chern-Simons theory. Furthermore, we show that the ...  
[xxx.lanl.gov/pdf/hep-th/0004116](http://xxx.lanl.gov/pdf/hep-th/0004116) - Similar pages

Citations: The architecture of the Alliant FX/8 computer - Perron ...

... C3 Series [2] and Alliant [11] only have 8 **logical vector** registers. ... such as the Alliant FX 8 [102] and the Sequent **Symmetry** [80] all processors are ...  
[citeseer.csail.mit.edu/context/118190/0](http://citeseer.csail.mit.edu/context/118190/0) - 18k - Cached - Similar pages

Syntax

... applies the specified crystallographic **symmetry** operator to the selected